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This book provides readers with fundamental assembly language programming skills, an understanding of the functional hardware components of a microcontroller, and skills to interface a variety of external devices with microcontrollers. KEY TOPICS: Chapter topics cover an introduction to the 68HC12, 68HC12 assembly language programming, advanced assembly programming, fuzzy logic, hardware ...

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1 Introduction To The HC 12 Microcontrollers 1.1 Brief History of Computers 1.2 Computers and Embedded Controllers 1.3 Applications of Embedded Controllers 1.4 Overview of the HC12 1.4.1 Software Instruction Set 1.4.2 Hardware 1.5 Overview of the S12

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About the HC-12. The HC-12 is a half-duplex 20 dBm (100 mW) transmitter paired with a receiver that has -117 dBm (2×10^{-15} W) sensitivity at 5000 bps.. Paired with an external antenna, these transceivers are capable of communicating up to and possibly slightly beyond 1 km in the open and are more than adequate for providing coverage throughout a typical house.

Understanding and Implementing the HC-12 Wireless ...

Prentice Hall- Computers - pages. Microcontroller Theory and Applications: HC12 and S12, 2nd Edition. Chapter topics cover an introduction to the 68HC12, 68HC12 assembly language programming, advanced assembly programming, fuzzy logic, hardware configuration, exception — resets and interrupts, the 68HC12 clock module and standard timer module TIMthe 68HC12 memory system, analog-to-digital ...

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microcontroller to use for a given application. Since costs are important, it is only logical to select the cheapest device that matches the application's needs. As a result, microcontrollers are generally tailored for specific applications, and there is a wide variety of microcontrollers to choose from.

Introduction to Microcontrollers

7 The HC12 and S12 Clock and Timer System 7.1 Overview . 7.2 Background Theory . 7.3 The HC12 Clock Module. 7.4 HC12 Clock Divider Chain Serving the TIM. 7.5 The HC12 Timer Module. 7.6 Components of the HC12 Timer Module. 7.6.1 HC12 Free-Running Counter . 7.6.2 HC12 Free-Running Counter-Associated Registers. 7.6.3 Input Capture/Output Compare ...

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